A Charge Controller Design For Solar Power System

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Abstract: This paper presents the solar charge controller circuit for controlling the overcharging and discharging from solar panel. This circuit regulates the charging of the battery in a solar system by monitoring battery voltage and switching the solar or other power source off when the battery reaches a preset voltage. This circuit is low voltages disconnect circuit. A charge controller circuit can increase battery life by preventing over-charging which can cause loss of electrolyte. The flow chart is also provided.

Keywords: Solar panel, a charge controller, battery

I. INTRODUCTION

Sometimes, electricity can be cut-off due to natural disasters aften happened. So, this type of energy such as wind energy, hydrulic energy and solar energy, they are needed as the source of electricity. Solar energy has become a promising alternative source because it has many advantages such as abundance, pollution free and renewability. The advantages of choosing solar energy by using a solar panel are thet the lifetime of solar panel is long lasting than any other source of energy. The solar charging system is very important and necessary to use for people live in rural areas. The sun is a cheap source of electricity because instead of hydralic generators it uses solar cells to produce electricity. Nowaday, we use the sun as a natural source of energy. Solar resource is umnlimited the government is trying to implement the use of solar panels as energy source in rural and sub urban areas for lighting the street lights, but the battery used to store the power gets affected due to overcharge and discharges.

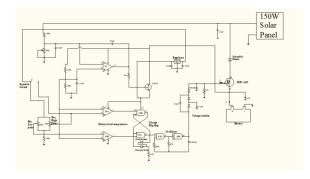


Fig.1. The circuit design of the charge controller

The solar power will play and important role in alleviating the energy crisis and reducing the environment pollution and has a bright propspect of applications. A charge controller is an essential part of nearly all power systems the charge batteries, wind, hydro, fuel, or utility grid. Its purpose is to keep the battery properly fed and safe fo long term. Acharge controller is a regulator that goes between the solar panels and the batteries. Regulators for solar systems are designed to keep the batteried charged at peak without overcharging.

II. CIRCUIT OPERATION OF CHARGE CONTROLLER DESIGN

In my circuit design, during charging, current flows from the solar panel through the diode, MOSFET, fuse F₁ and into the battery. Power MOSFET transistor is the main switching device in the charge controller circuit. It connects the solar panel to the battery when it is in need of charging and power is available from the solar panel. Diode is a Schottky device preventing back currents flowing from the battery to the solar panel. Fuse F₁ provides a safely limit on the current available from the battery in the event of a short. Comparator U₂ is used to control power to the rest of the circuit. When the solar panel voltage is lower than the battery voltage, the rest of the circuitry is disabled. When the solar panel voltage rises above the battery voltage, the output of U₂ goes on, switching on transistor which provides power to the rest of the circuit. Resistor networks scale the battery and the solar panel voltages to a range that is useful to U₂. Voltage regulator is used as a reference for the battery set points, the reference points are adjusted the resistor network in U_{1A} and U_{1B} . Comparators U_{1A} and U_{1B} monitor the battery voltage and switch states when the battery is fully charged or has dropped to a voltage where charging should resume. The comparators drive as a setreset flip-flop circuit consisting of U_{3A} and U_{3B}. The output of flip-flop is used to turn the oscillator consisting of U_{3C} and U_{3D} on and off. The flip-flop also drives the two LEDs used to indicate charging or battery full states. The oscillator generates a 10 kHz square wave and the voltage doubler circuit will stepped up the voltage to turn the MOSFET on fully. An equalize switch is included to allow for overcharging of the battery by raising the threshold of the high voltage sensing comparator, forcing the charge current on.

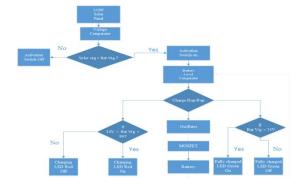


Fig.2. The flow chart of charge controller

III. CALCULATION AND SIMULATION

1. Calculation for 741 op-amp

For Inverting pin,

 $R_1=100k\Omega$, $R_2=100k\Omega$, $+V_{bat}=12V$

$$(-)vtg = \frac{100k}{100k+100k} \times 12 = 6V$$

For Non-inverting pin, When solar panel vtg= 5V,

 $R_3=100k\Omega$, $R_4=100k\Omega$

$$(+)$$
vtg= $\frac{100k}{100k+100k}$ ×5=2.5V

(+)vtg< (-)vtg,

2.5V <6V, **Vout** = 0 V

When solar panel vtg= 12V,

$$(+)$$
vtg= $\frac{100k}{100k+100k}$ ×12= 6V

(+)vtg = (-)vtg,

6V = 6V, **Vout** = 0V

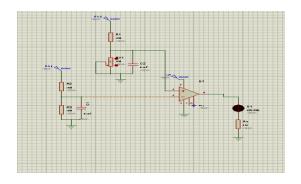


Fig.3. The simulation result when the output at 5V and 12V

When solar panel vtg= 13.5V,

$$(+)vtg = \frac{100k}{100k + 100k} \times 13 = 6.75V$$

(+)vtg>(-)vtg,

6.75V > 6V, **Vout** = 10.96 V (11V)

When solar panel vtg= 13.5V,

$$(+)$$
vtg= $\frac{100k}{100k+100k}$ ×15= 7.5V

(+)vtg> (-)vtg

7.5V > 6V, **Vout** = 10.96 V (11V)

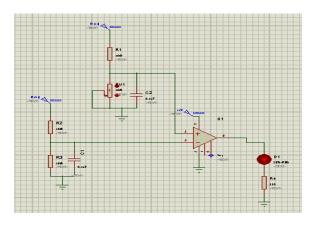


Fig.4.The simulation result when the output at 13.5V and 15V

2. Calculation for battery level comparator

At the battery low point,

Let battery low level point = 9V

For upper comparator,

Inverting comparator,

 V_{in} , (-) pin =4.5V

V_{ref}, (+) pin=4.8V

 $V_{in} < V_{ref}, V_{out} = 1$

For lower comparator,

Non-inverting comparator,

 $V_{in.}(+)$ pin=4.5V

V_{ref}, (-) pin=4.8V

 $V_{in} < V_{ref}, V_{out} = 0$

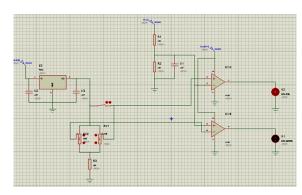


Fig.5.The simulation result when the battery level output at low stage= 9V

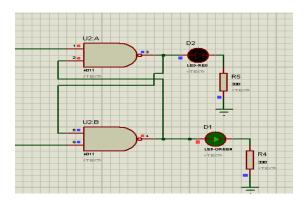


Fig.6.The simulation result of the flip-flop output at low stage= 9V

At the battery high point,

Let the battery high level point= 13V

For upper comparator,

Inverting comparator,

 V_{in} , (-) pin =6.5V

V_{ref}, (+) pin=4.8V

 V_{in} > V_{ref} , V_{out} =0

For lower comparator,

Non-inverting comparator

 $V_{in.}(+)$ pin=6.5V

V_{ref}, (-) pin=4.8V

 $V_{in}>V_{ref}$, $V_{out}=1$

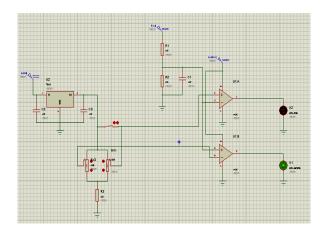


Fig.7.The simulation result when the battery level output at high stage= 13V

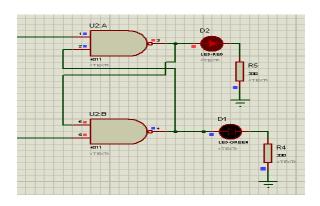


Fig.8.The simulation result of the flip-flop output at high stage= 13V

3. Calculation for battery level comparator

 $R = 47k\Omega$, 0.001uF

T=2.2RC

 $=2.2 \times 47k \times 0.001u$

=0.1ms

 $F=\frac{1}{T}$

 $=\frac{1}{0.1m}$

= 10 kHz

4. Calculation for MOSFET

C rate = 70A/H

The desired current = 0.1C= 0.1×70 =7A

The gate voltage $=V_G$ The battery voltage $=V_S$ The threshold vtg $=V_T$

$$K = \frac{ID}{(VGS-VT)2} = \frac{7}{(3-0)2} = 0.78A/V2$$

where,
$$V_G = 12V$$
, $V_S = 9V$
 $V_{GS} = 12 - 9 = 3V$
 $V_T = 0V$

When
$$V_S = 9.5V$$
, $V_{GS} = 12 - 9.5 = 2.5V$
 $I_D = k (VGS - VT)^2 = 0.78(2.5 - 0)^2$
=4.8A

When
$$V_S = 10V$$
, $V_{GS} = 12 - 10 = 2V$
 $I_D = k (VGS - VT)^2 = 0.78(2 - 0)^2$
 $= 3.12A$

When
$$V_S = 11V$$
, $V_{GS} = 12 - 11 = 1V$
 $I_D = k \left(VGS - VT\right)^2 = 0.78 \left(1 - 0\right)^2$

=1V

When
$$V_S = 12V$$
, $V_{GS} = 12 - 12=0V$
 $I_D = 0 A$

IV. CONCLUSION

As the solar power system is very advantageous and solar recourse is unlimited of various countries tried to take the use of such recourses but they faced many difficulties in actually implementing the technology. So, every country tried to make to control the battery charging with many types of charge controller design that is low size and cost. The control of battery charging is so important the most manufactures of high quality batteries specify the requirements for voltage regulation, low voltage disconnect and temperature compensation. When these limits are not respected, it is common for batteries to fail after less than one quarter of their normal life expectancy, regardless of their quality or their cost. A good charge controller is not expensive in relation to the total cost of a power system.

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